What is claimed is:

1. A liquid crystal display device, comprising:

a plurality of pixels arranged in a matrix;

a drain line provided for each column of said plurality of pixels;

a gate line provided for each row of said plurality of pixels;

an output buffer to output a video signal to be supplied to said drain line; and

a video correction signal generator to superpose a correction signal on the output signal of said output buffer.

2. The liquid crystal display device according to claim 1, wherein said video correction signal generator superposes said correction signal on the output signal of said output buffer at least in one of the rising and falling of the output signal.

3. The liquid crystal display device according to claim 1, wherein said video correction signal generator changes a waveform of said correction signal in synchronization with the timing in which said gate line is driven.

4. The liquid crystal display device according to claim 1, wherein

said pixel includes a thin film transistor having a drain connected to said drain line, and a resistive element connected in series to a source of said thin film transistor, and

the resistance value of said resistive element is reduced as the length of said drain line between the pixel and said video correction signal generator increases.

5. The liquid crystal display device according to claim 1,

wherein

said video correction signal generator includes a differentiator to differentiate the output signal of each said output buffer, and an adder to add the output signal of each said differentiator and the output signal of each said output buffer, and

said video correction signal generator outputs the output signal of each said adder to a corresponding one of said drain lines.

6. The liquid crystal display device according to claim 1, wherein

said video correction signal generator includes a differentiator to differentiate an externally input reference pulse, and an adder to add the output signal of the differentiator and the output signal of each said output buffer, and

said video correction signal generator outputs the output signal of each said adder to a corresponding one of said drain lines.

7. The liquid crystal display device according to claim 1, wherein said video correction signal generator includes a correction signal changing system to change a waveform of said correction signal in association with the length of said drain line between a pixel to be supplied with said video signal and itself.

8. The liquid crystal display device according to claim 1, wherein said video correction signal generator includes:

a differentiator to differentiate the output signal of

said output buffer;

an integrator to integrate the output signal of said differentiator and output the result of integration in association with an input first disenable signal;

an inverting integrator to invert and integrate the output signal of said differentiator and output the result of integration in association with an input second disenable signal;

a first adder to add the output signal of said integrator and the output signal of said inverting integrator; and

a second adder to add the output signal of said first adder and the output signal of said output buffer.

9. The liquid crystal display device according to claim 1, wherein said video correction signal generator includes:

a differentiator to differentiate an externally input reference pulse;

an integrator to integrate the output signal of said differentiator and output the result of integration in association with an input first disenable signal;

an inverting integrator to invert and integrate the output signal of said differentiator and output the result of the integration in association with an input second disenable signal;

a first adder to add the output signal of said integrator and the output signal of said inverting integrator; and

a second adder to add the output signal of said first adder and the output signal of said output buffer.

10. The liquid crystal display device according to claim 7, wherein said video correction signal generator includes:

a differentiator to differentiate the output signal of

said output buffer;

an integrator to integrate the output signal of said differentiator and output the result of integration in association with an input first disenable signal;

an inverting integrator to invert and integrate the output signal of said differentiator and output the result of integration in association with an input second disenable signal;

a first adder to add the output signal of said integrator and the output signal of said inverting integrator; and

a second adder to add the output signal of said first adder and the output signal of said output buffer.

11. The liquid crystal display device according to claim 7, wherein said video correction signal generator includes:

a differentiator to differentiate an externally input reference pulse;

an integrator to integrate the output signal of said differentiator and output the result of integration in association with an input first disenable signal;

an inverting integrator to invert and integrate the output signal of said differentiator and output the result of integration in association with an input second disenable signal;

a first adder to add the output signal of said integrator and the output signal of said inverting integrator; and

a second adder to add the output signal of said first adder and the output signal of said output buffer.

12. The liquid crystal display device according to claim 5, wherein said differentiator includes a shift register, and a potential switching system to switch a potential at an output end

in association with the output signal of said shift register.